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☐ 1. Document ID: US 4860191 A

L1: Entry 1 of 8   File: USPT   Aug 22, 1989

US-PAT-NO: 4860191
DOCUMENT-IDENTIFIER: US 4860191 A

TITLE: Coprocessor with dataflow circuitry controlling sequencing to execution unit of
data received in tokens from master processor

DATE-ISSUED: August 22, 1989

INT-CL: [4] G06F 15/16, G06F 15/20, G06F 13/38

US-CL-ISSUED: 364/200
US-CL-CURRENT: 710/110; 712/201

FIELD-OF-SEARCH: 364/2MSFile, 364/9MSFile

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
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☐ 2. Document ID: US 4811345 A

L1: Entry 2 of 8   File: USPT   Mar 7, 1989

US-PAT-NO: 4811345
DOCUMENT-IDENTIFIER: US 4811345 A

TITLE: Methods and apparatus for providing a user oriented microprocessor test
interface for a complex, single chip, general purpose central processing unit

DATE-ISSUED: March 7, 1989

INT-CL: [4] G06F 11/00

US-CL-ISSUED: 371/16; 364/200
US-CL-CURRENT: 714/27

FIELD-OF-SEARCH: 371/15, 371/16, 371/17, 371/18, 371/20, 324/73R, 324/73AT, 364/2MSFile

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
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☐ 3.   Document ID: US 4799345 A

L1: Entry 3 of 8   File: USPT   Jan 24, 1989

US-PAT-NO: 4799345
DOCUMENT-IDENTIFIER: US 4799345 A

TITLE: Reversible compression seal

DATE-ISSUED: January 24, 1989

INT-CL: [4] E04C 11/10

US-CL-ISSUED: 52/396; 52/403, 404/65, 404/66
US-CL-CURRENT: 52/396.06; 404/65, 404/66

FIELD-OF-SEARCH: 52/403, 52/396, 404/64, 404/65, 404/66

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
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☐ 4.   Document ID: US 4641238 A

L1: Entry 4 of 8   File: USPT   Feb 3, 1987

US-PAT-NO: 4641238
DOCUMENT-IDENTIFIER: US 4641238 A

TITLE: Multiprocessor system employing dynamically programmable processing elements
controlled by a master processor

DATE-ISSUED: February 3, 1987

INT-CL: [4] G06F 15/16

US-CL-ISSUED: 364/200
US-CL-CURRENT: 710/110; 902/2, 902/39

FIELD-OF-SEARCH: 364/200, 364/900

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 5. Document ID: US 4594651 A

L1: Entry 5 of 8   File: USPT   Jun 10, 1986

US-PAT-NO: 4594651
DOCUMENT-IDENTIFIER: US 4594651 A

TITLE: Concurrent processor for control
   L1: Entry 5 of 8   File: USPT   Jun 10, 1986

US-PAT-NO: 4594651
DOCUMENT-IDENTIFIER: US 4594651 A

TITLE: Concurrent processor for control

DATE-ISSUED: June 10, 1986

INT-CL: [4] G06F 9/00

US-CL-ISSUED: 364/131; 364/180
US-CL-CURRENT: 700/2

FIELD-OF-SEARCH: 364/131, 364/135, 364/2MSFile, 364/180, 364/160, 364/704, 318/590, 318/596

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 6. Document ID: US 4541048 A

L1: Entry 6 of 8   File: USPT   Sep 10, 1985

US-PAT-NO: 4541048
DOCUMENT-IDENTIFIER: US 4541048 A

TITLE: Modular programmable signal processor
   L1: Entry 6 of 8   File: USPT   Sep 10, 1985

US-PAT-NO: 4541048
DOCUMENT-IDENTIFIER: US 4541048 A

TITLE: Modular programmable signal processor

DATE-ISSUED: September 10, 1985

INT-CL: [3] G06F 7/38, G06F 15/00

US-CL-ISSUED: 364/200; 364/736
US-CL-CURRENT: 712/32; 708/524

FIELD-OF-SEARCH: 364/200, 364/736

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 7.   Document ID: US 4276594 A

L1: Entry 7 of 8   File: USPT   Jun 30, 1981

US-PAT-NO: 4276594
DOCUMENT-IDENTIFIER: US 4276594 A

TITLE: Digital computer with multi-processor capability utilizing intelligent composite
memory and input/output modules and method for performing the same

DATE-ISSUED: June 30, 1981

INT-CL: [3] G06F 15/16, G06F 13/00

US-CL-ISSUED: 364/200
US-CL-CURRENT: 713/600

FIELD-OF-SEARCH: 364/2MSFile, 364/9MSFile

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
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☐ 8.   Document ID: US 3631405 A

L1: Entry 8 of 8   File: USPT   Dec 28, 1971

US-PAT-NO: 3631405
DOCUMENT-IDENTIFIER: US 3631405 A

TITLE: SHARING OF MICROPROGRAMS BETWEEN PROCESSORS

DATE-ISSUED: December 28, 1971

INT-CL: [] G06f 9/12, G06f 15/16

US-CL-ISSUED: 340/172.5
US-CL-CURRENT: 712/209; 709/310, 709/312, 712/226, 712/229, 712/246, 712/247

FIELD-OF-SEARCH: 340/172.5

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4594651S	0
"4641238".USPT.	1
4641238S	0
"4799345".USPT.	1
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